



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#8

4/17/03  
VJ

Application Serial No. .... 09/755,673  
Filing Date ..... 1/5/2001  
Inventor ..... Forbes et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2823  
Examiner ..... Nguyen, K.D.  
Attorney's Docket No. .... MI22-1531  
Title:       Methods of Forming Capacitor Structures, and Capacitor Structures

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 4/3/03

By: [Signature]  
Robert C. Hyta  
Reg. No. 46,791

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